

100

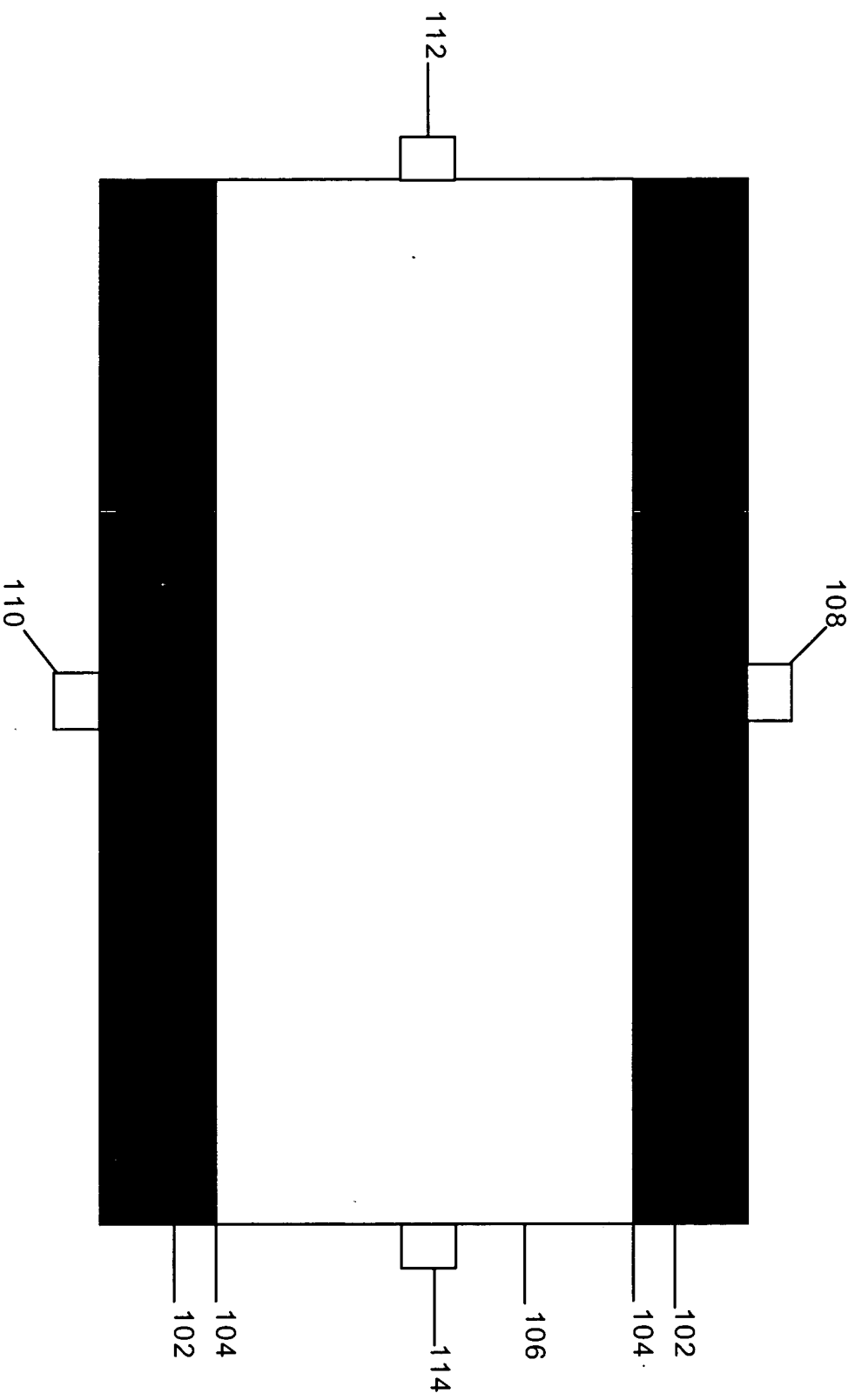


FIG. 1 (PRIOR ART)

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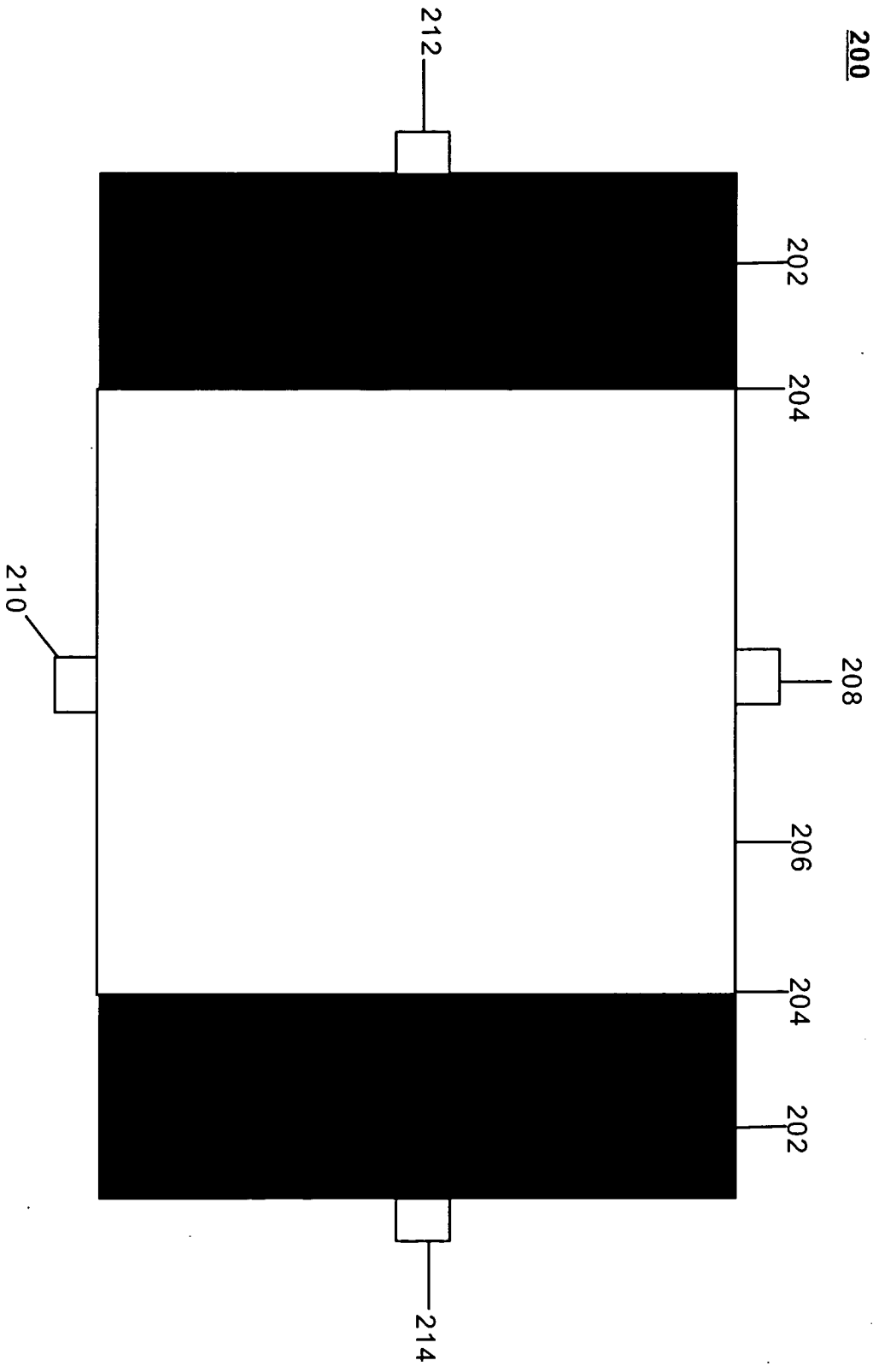


FIG. 2 (PRIOR ART)

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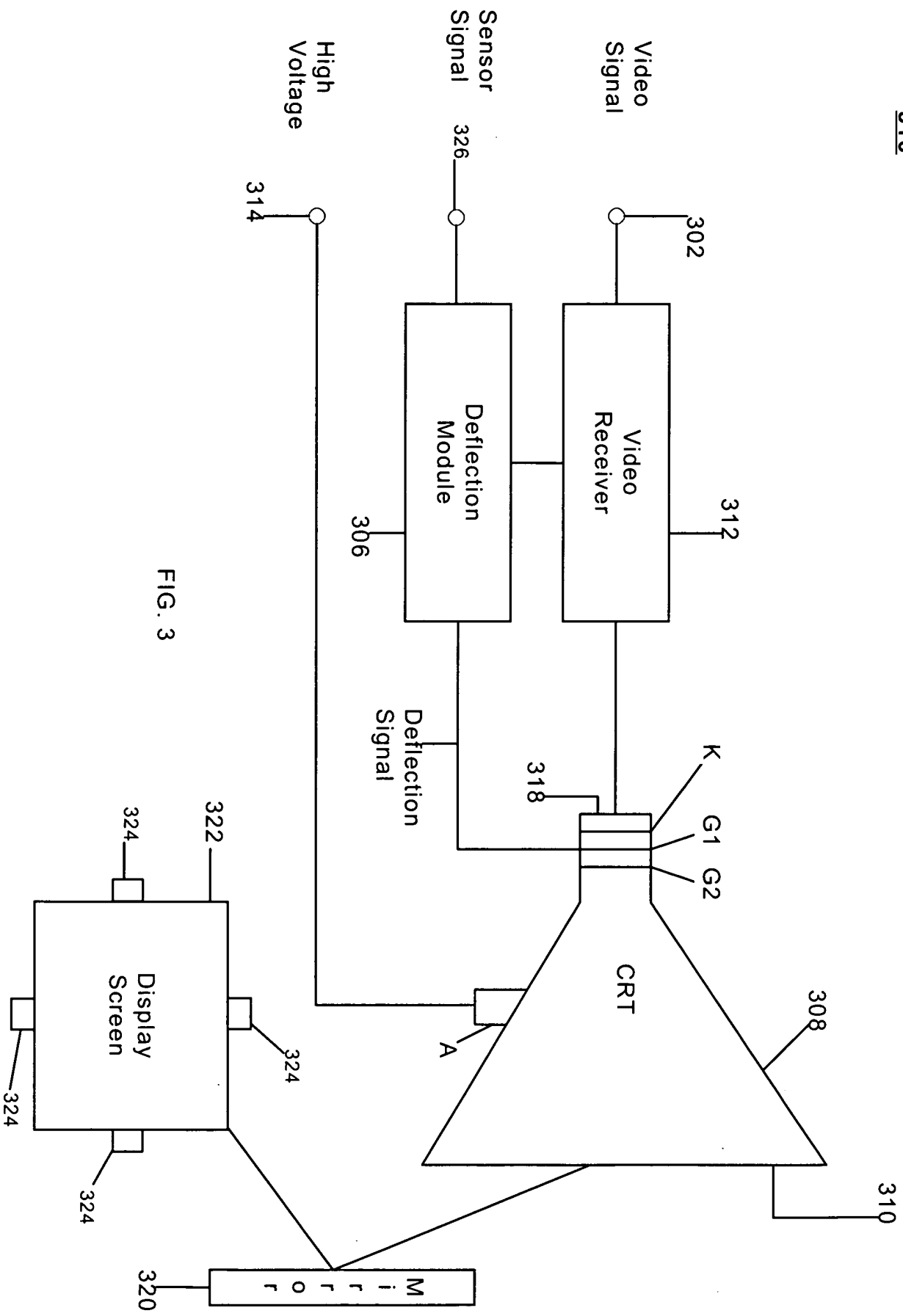


FIG. 3

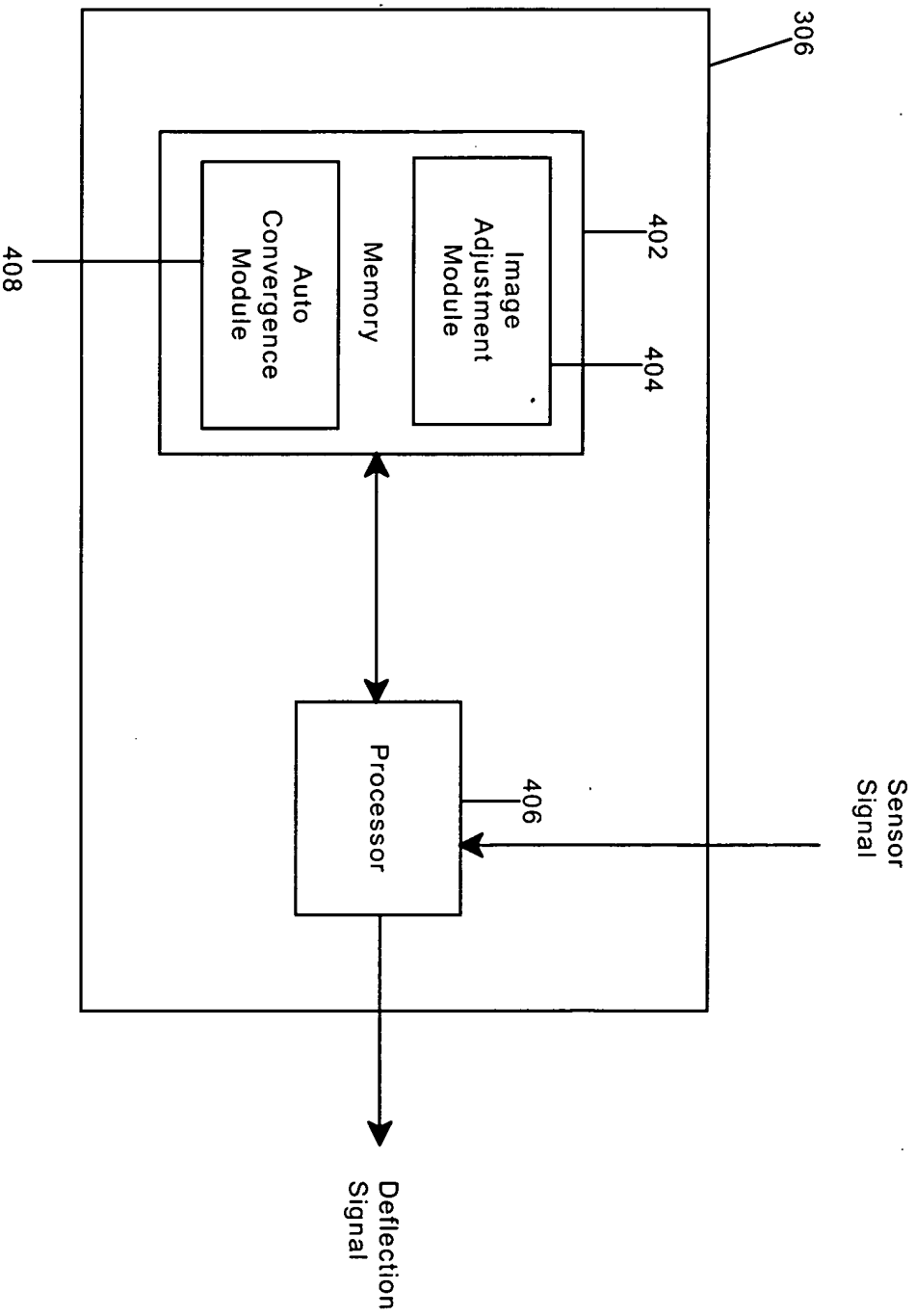


FIG. 4

FIG. 4 is a block diagram of a system 306, which includes an image adjustment module, a memory module, an auto convergence module, and a processor. The image adjustment module, memory module, and auto convergence module are connected to the processor. The processor is connected to a sensor signal and a deflection signal.

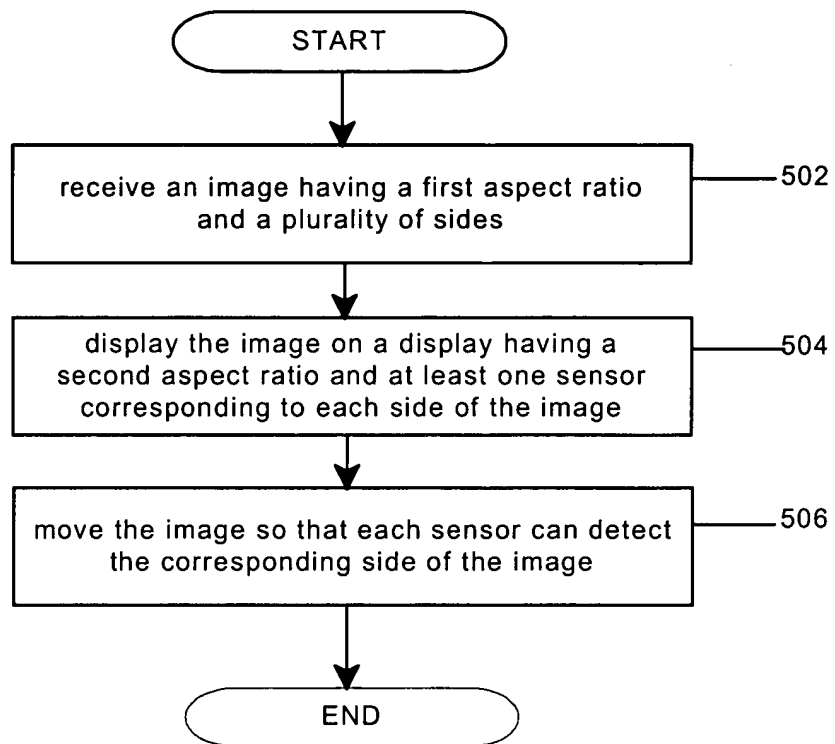


FIG. 5

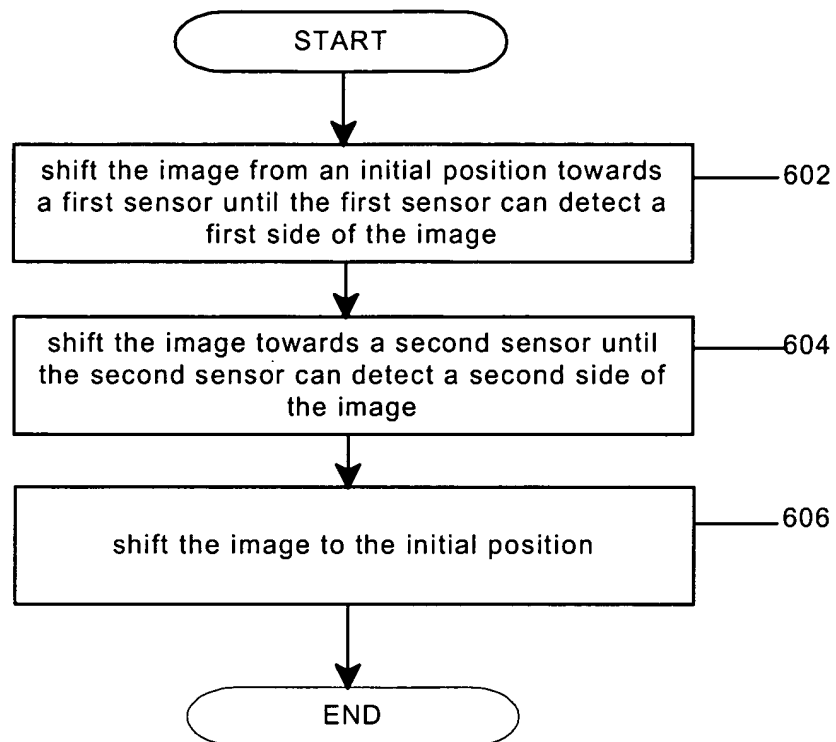


FIG. 6

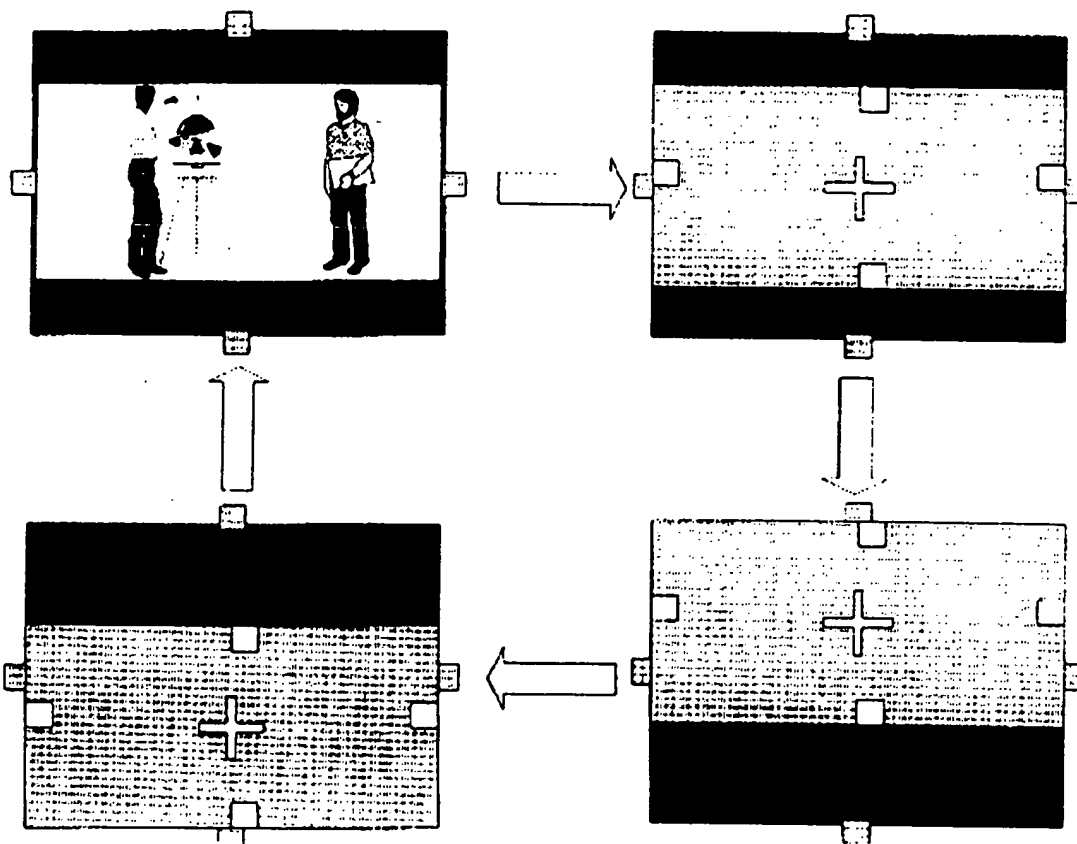


FIG. 7

**506**

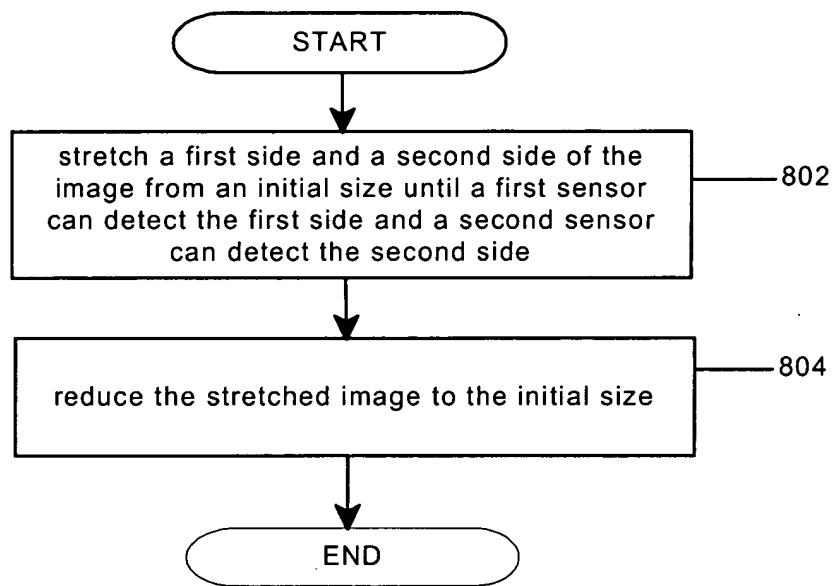


FIG. 8



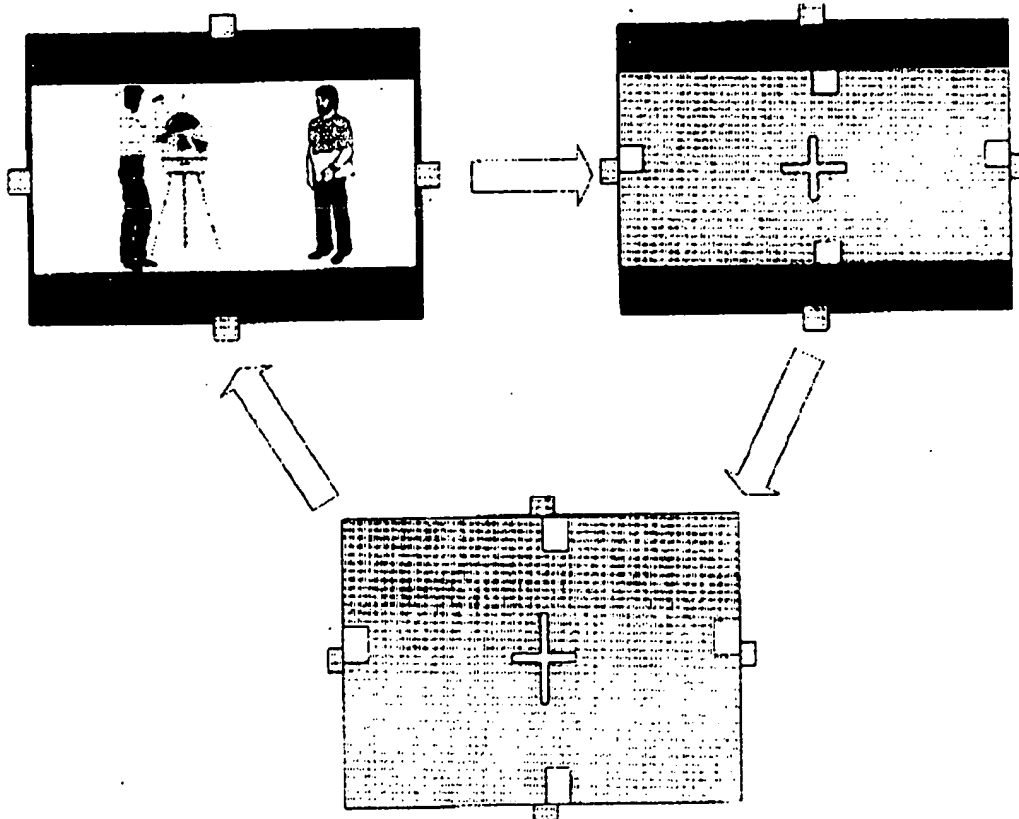


FIG. 9